Assistant Commissioner for Patents

Box Patent Application

Atty. Docket No.: First Inventor:

US03 0060 Hars, Laszlo

Washington, D.C. 20231

Express Mail No.: EV 417136683 US

Title: VLSI IMPLEMENTATION OF A RANDOM NUMBER GENERATOR USING A

PLURALITY OF SIMPLE FLIP-FLOPS

Sir:

This application claims priority to Provisional Serial No. 60/454,838 filed March 14, 2003.

ENCLOSED ARE:

Copy of this transmittal, which includes authorization to charge fees to deposit account 14-1270 according to 37 CFR §1.136(a)(3);

Specification (11 pages including Spec., Claims & Abstract);

Drawings (4 sheet(s));

Un-signed Declaration and Power of Attorney (2 pages); and

Appointment of Associates.

FEE COMPUTATION

Claims as filed					
· For	# filed		# extra	Rate	Fee
Total Claims	20	- 20 =	0	× \$18 =	\$00.00
Independent Claims	2	- 3 =	0	× \$86 =	\$00.00
Basic Fee		-		=	\$770.00
TOTAL Filing Fee				=	\$770.00

Correspondence Address. Please use Customer Number 24738 for all correspondence regarding this application. Philips Electronics North America Corporation, Intellectual Property & Standards, 1109 McKay Drive, M/S-41SJ, San Jose, CA 95131. Phone: (408) 474-9073, Fax: (408) 474-9082.

PATENT TRADEMARK OFFICE

Please charge Deposit Account No. 14-1270 for the total filing fee in the amount calculated above. The Commissioner is hereby requested and authorized pursuant to 37 CFR §1.136(a)(3), to treat any concurrent or future reply in this application requiring a petition for extension of time for its timely submission, as incorporating a petition for extension of time for the appropriate length of time. Please charge any additional fees which may now or in the future be required in this application, including extension of time fees, but excluding the issue fee unless explicitly requested to do so, and credit any overpayment, to Deposit Account No. 14-1270.

Date March 15, 2084

Respectfully submitted,

Michael E. Schmitt, Reg. No. 36,92